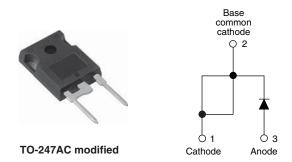
**Vishay Semiconductors** 



### Hyperfast Rectifier, 30 A FRED Pt®



PRODUCT SUMMARY							
Package	TO-247AC modified (2 pins)						
I <sub>F(AV)</sub>	30 A						
V <sub>R</sub>	600 V						
V <sub>F</sub> at I <sub>F</sub>	2.6 V						
t <sub>rr</sub> typ.	See Recovery table						
T <sub>J</sub> max.	175 °C						
Diode variation	Single die						

#### FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Single diode device
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

#### **DESCRIPTION/APPLICATIONS**

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Peak repetitive reverse voltage	V <sub>RRM</sub>		600	V					
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 116 °C	30	٨					
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	300	A					
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C					

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>			-	-					
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 30 A -		2.0	2.6	V				
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	1.34	1.75					
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> = V <sub>R</sub> rated	-	0.3	50					
		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	60	500	μA				
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	33	-	pF				
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	3.5	-	nH				

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Pb-free (e3)



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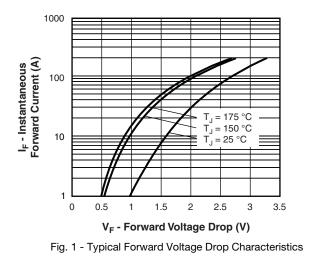


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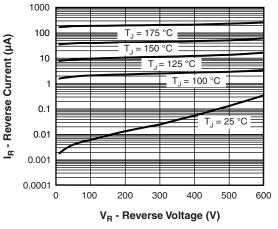
<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS				
Reverse recovery time	t <sub>rr</sub>	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t =$	-	28	35					
		T <sub>J</sub> = 25 °C		-	31	-	ns A			
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 30 A dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 200 V	-	77	-				
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	3.5	-				
		T <sub>J</sub> = 125 °C		-	7.7	-				
Reverse recovery charge	Q <sub>rr</sub>	$T_J = 25 \ ^{\circ}C$		-	65	-	nC			
		T <sub>J</sub> = 125 °C		-	345	-				

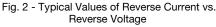
THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C				
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	0.5	0.9					
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W				
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.4	-	-				
Weight			-	6.0	-	g				
Weight			-	0.22	-	oz.				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Marking device		Case style TO-247AC modified	30EPH06							

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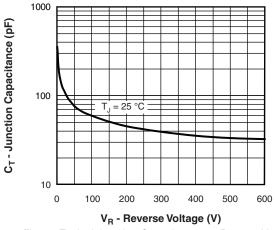


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

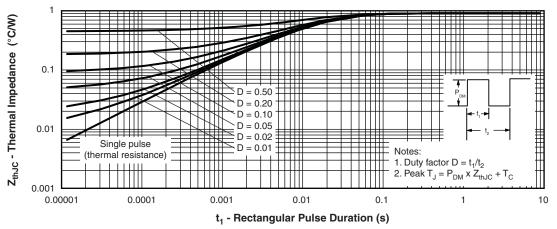


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

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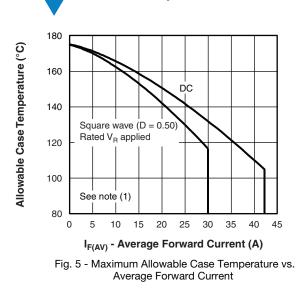
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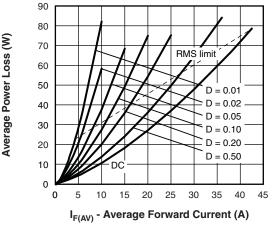
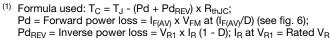
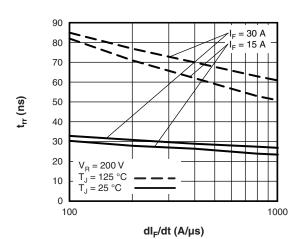


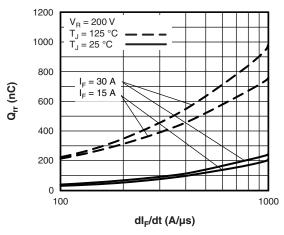
Fig. 6 - Forward Power Loss Characteristics

#### Note











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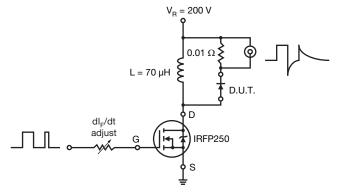


Fig. 9 - Reverse Recovery Parameter Test Circuit

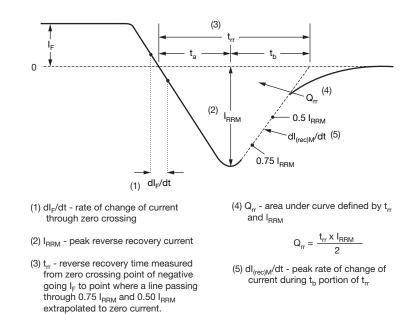


Fig. 10 - Reverse Recovery Waveform and Definitions

30 E P H 06 PbF

7

1 - Vishay Semiconductors product

(4)

- Current rating (30 = 30 A)

(3)

- Circuit configuration:
- E = Single diode
- Package:

(2)

VS-

1

2

3

4

7

- P = TO-247AC modified
- 5 H = Hyperfast recovery
- 6 Voltage rating (06 = 600 V)
  - Environmental digit:
    - PbF = Lead (Pb)-free and RoHS compliant

(5)

(6)

-N3 = Halogen-free, RoHS compliant and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-30EPH06PbF	25	500	Antistatic plastic tube						
VS-30EPH06-N3	25	500	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS								
Dimensions		www.vishay.com/doc?95253						
Part marking information	TO-247AC modified PbF	www.vishay.com/doc?95255						
	TO-247AC modified -N3	www.vishay.com/doc?95442						

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**ORDERING INFORMATION TABLE** 

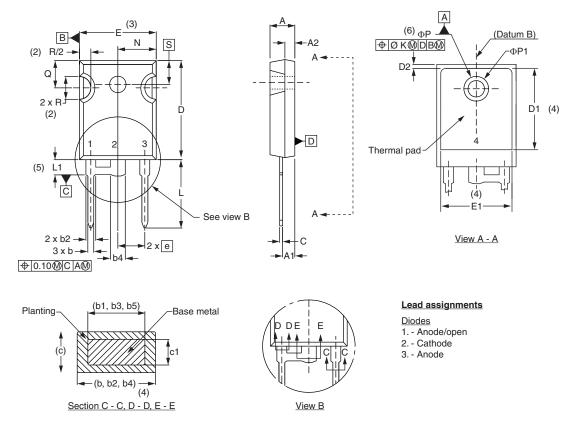
**Device code** 

### **Outline Dimensions**





#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STINDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ΦK	2.	54	0.0	)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.37	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			N	7.62	BSC	0.3		
b5	2.59	3.38	0.102	0.133			ΦР	3.56	3.66	0.14	0.144	
С	0.38	0.86	0.015	0.034			Φ <b>P1</b>	-	6.98	-	0.275	
c1	0.38	0.76	0.015	0.030			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	1.78	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	

#### Notes

<sup>(1)</sup> Dimensioning and tolerance per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

- <sup>(4)</sup> Thermal pad contour optional with dimensions D1 and E1
- <sup>(5)</sup> Lead finish uncontrolled in L1

(6)  $\Phi P$  to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC outline TO-247 with exception of dimension c

Revision: 21-Jun-11

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Document Number: 95253

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